

Form PTO-1449 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE							ATTY DOCKET NO. 0325.00446		SERIAL NO. 09/775,372			
INFORMATION DISCLOSURE STATEMENT BY APPLICANT							APPLICANT: Jiann-Cheng Chen et al.					
							FILING DATE February 1, 2000		GROUP 2151			
MAY 28 2004							U.S. PATENT DOCUMENTS					
Examiner TRADEMARK OFFICE	Document No.			Date	Name			Class	Sub-Class	Filing Date		
SKS	6	5	7	8	1	1	8	6-10-2003	Raza et al.	711	154	9-29-2000
SKS	6	6	3	1	4	5	5	10-7-2003	Raza et al.	711	170	9-29-2000
SKS	6	5	8	1	1	4	4	6-17-2003	Raza et al.	711	166	9-29-2000
SKS	6	6	2	5	7	1	1	9-23-2003	Raza et al.	711	173	11-16-00
SKS	6	6	2	9	2	2	6	9-30-2003	Paul et al.	711	169	12-8-2000
SKS	6	7	1	5	0	2	1	3-30-2004	Paul et al.	710	310	12-8-2000
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FOREIGN PATENT DOCUMENTS												
	Document No.			Date	Country			Class	Sub-Class	Transl.		
										Yes	No	
OTHER DOCUMENTS (Incl. Author, Title, Date, Pertinent Pages, etc.)												
SKS	S. Babar Raza et al., "ARCHITECTURE FOR IMPLEMENTING VIRTUAL MULTIQUEUE FIFO", Serial No. 09/676,704, Filed September 29, 2000.											
SKS	S. Babar Raza et al., "LOGIC FOR GENERATING MULTICAST/UNICAST ADDRESS (ES)", Serial No. 09/676,706, Filed September 29, 2000.											
SKS	Somnath Paul et al., "FIFO READ INTERFACE PROTOCOL", Serial No. 09/732,686, Filed December 8, 2000.											
SKS	S. Babar Raza et al., "LOGIC FOR PROVIDING ARBITRATION FOR SYNCHRONOUS DUAL-PORT MEMORY", Serial No. 09/676,169, Filed September 29, 2000.											
Examiner Suresh K Suryawanshi				Date Considered 8/2/04								
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